TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

# 2SK1529

## **High-Power Amplifier Application**

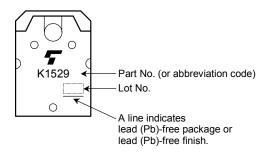
• High breakdown voltage  $$:V_{DSS} = 180V$$ • High forward transfer admittance  $:|Y_{fs}| = 4.0 \text{ S (typ.)}$ 

• Complementary to 2SJ200

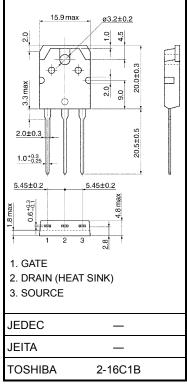
## **Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Drain-source voltage	$V_{DSS}$	180	V
Gate-source voltage	$V_{GSS}$	±20	V
Drain current (Note 1)	$I_{D}$	10	Α
Drain power dissipation (Tc = 25°C)	$P_{D}$	120	W
Channel temperature	T <sub>c</sub>	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

# Marking



#### Unit: mm



Weight: 4.6 g (typ.)

# **Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 180 V, V <sub>GS</sub> = 0	_	_	1.0	mA
Gate leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0, V <sub>GS</sub> = ±20 V	_	_	±0.5	μΑ
Drain-source breakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0	180	_	_	V
Drain-source saturation voltage	V <sub>DS</sub> (ON)	I <sub>D</sub> = 6 A, V <sub>GS</sub> = 10 V	_	2.5	5.0	V
Gate-source cut-off voltage (Note 2)	V <sub>GS (OFF)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.1 A	0.8	_	2.8	V
Forward transfer admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3 A	_	4.0	_	S
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0, f = 1 MHz	_	700	_	
Output capacitance	Coss	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0, f = 1 MHz	_	150	_	pF
Reverse transfer capacitance	C <sub>rss</sub>	V <sub>DD</sub> = 30 V, V <sub>GS</sub> = 0, f = 1 MHz	_	90	_	

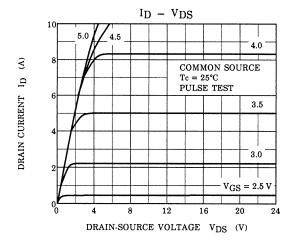
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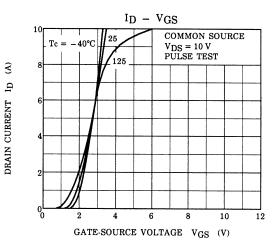
Note 1: Ensure that the channel temperature does not exceed 150°C.

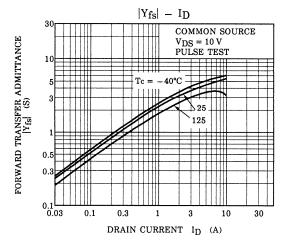
Note 2: V<sub>GS (OFF)</sub> Classification 0: 0.8~1.6 Y: 1.4~2.8

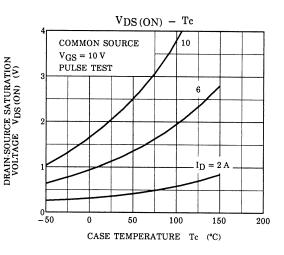
This transistor is an electrostatic-sensitive device.

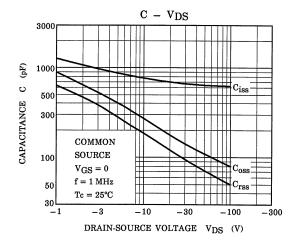
Please handle with caution.

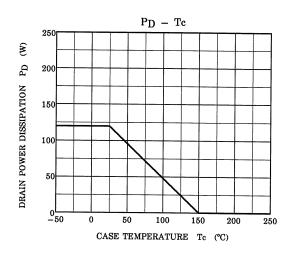


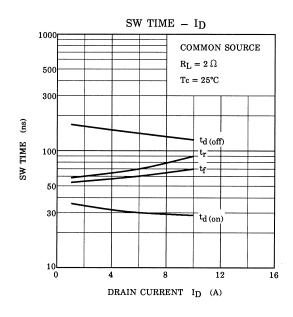


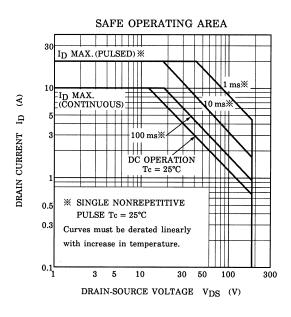




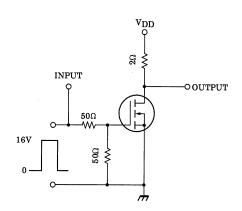




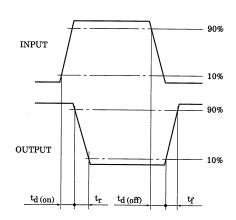




# **Switching Time Test Circuit**



### **Waveforms**



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